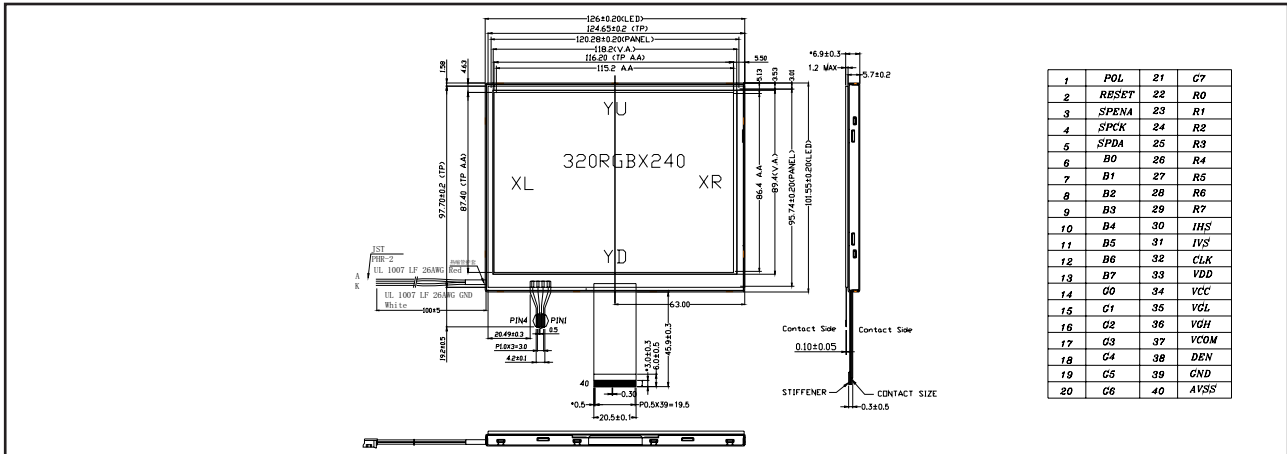


TFT TRANSMISSIVE LCD MODULES YTS 570ELAA-01-100T

5.7", 320 X 240 DOTS, 1/240 DUTY

EXTERNAL DIMENSION AND DISPLAY PATTERN



MECHANICAL DATA

ITEM	SPECIFICATION	UNIT
Module Size (W x H)	126.0 x 101.55 x 6.9	mm
Active Area (W x H)	118.26 x 89.40	mm
Viewing Direction	12:00	o'clock
Number of Dots	320 (RGB) x 240	dots
Colors	16M	

ABSOLUTE MAXIMUM RATINGS

PARAMETER	SYMBOL	MIN.	MAX.	UNIT
Supply Voltage ⁽¹⁾	V _{CC}	-0.3	7.0	V
Supply Voltage ⁽²⁾	V _{DD}	-0.3	7.0	V
Logic Output Voltage	V _{OUT}	-0.3	7.0	
Input Voltage	V _{IN}	-0.3	V _{DD} +0.3	V
Operating Temperature		See page 8		
Storage Temperature				

Note (1): All of the voltage listed above are with respect to GND=V_{SS}=0V
 Note (2): Device is subject to be damaged permanently if stresses beyond those absolute maximum ratings listed above.

PIN CONFIGURATION

PIN	SYMBOL	I/O	SIGNAL DESCRIPTION
1	POL	O	Polarity Signal to Monitor V _{COM} Signal
2	RESET	I	Hardware Reset
3	SPENA	I	The Pin of Touch Panel
4	SPCK	I	Serial Port Clock
5	SPDA	I/O	Serial Port Data Input/Output
6-13	B ₀ -B ₇	I	Blue Data Bit 0 to Blue Data Bit 7
14-21	G ₀ -G ₇	I	Green Data Bit 0 to Green Data Bit 7
22-29	R ₀ -R ₇	I	Red Data Bit 0 to Red Data Bit 7
30	IHS	I	Horizontal SYNC Input in Digital RGB Mode
31	IVS	I	Vertical SYNC Input in Digital RGB Mode
32	CLK	I	Clock Signal
33	V _{DD}	I	Analog Power
34	V _{CC}	I	Digital Power
35	V _{GL}	O	Negative Power Output Pin for Gate Driver
36	V _{GH}	O	Positive Power Output Pin for Gate Driver
37	V _{COM}	O	Power Supply for the TFT Display Common Electrode
38	DEN	I	Input Data Enable Control
39	GND	I	Digital Ground
40	AV _{SS}	I	Analog Ground

ELECTRICAL CHARACTERISTICS, Ta=25°C

ITEM	SYMBOL	CONDITION	SPEC. VALUE			UNIT
			MIN.	TYP.	MAX.	
TFT Gate ON Voltage	V _{GH}			15.0		V
TFT Gate OFF Voltage	V _{GL}	Ta=+25°C		-10.0		V
TFT Common Electrode Voltage	V _{comH} V _{comL}		2.5 -2.0		5.5 0	V

Note (3): V_{com} must be adjusted to optimize display quality: cross talk, contrast ratio and etc.
 Note (4): V_{GH} is TFT gate operating voltage.
 Note (5): V_{GL} is TFT gate operating voltage.
 Note (6): Environmental condition: 25°C±5°C.
 Note (7): Reference waveform for panel light on is s below: Gate width is 1.5 ms. V_{GH} / V_{OH} / V_{GL} / V_{COM} are referred to driver IC specification.
 Note (8): Operating Voltage V_{CC}=5.0V

BACKLIGHTING CHARACTERISTICS, Ta=25°C, LED

ITEM	SYMBOL	CONDITION	SPEC. VALUE			UNIT
			MIN.	TYP.	MAX.	
Forward Voltage	V _f	I _f =Typ.	9.0	9.6	10.2	V
Forward Current	I _f			140		mA
Luminous Intensity	L _v	I _f =Typ./Chip	4500	5000	5500	cd/m ²
Luminous Uniformity	ΔL _v	Ta=25°C	75			%
Chromaticity Coordinate	X		0.27	0.292	0.287	
	Y		0.27	0.270	0.310	

Note (9): Operating temperature range T_{opr} -30°C to +70°C; Storage temperature range T_{stg} -40°C to +85°C.

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TIMING OF POWER SUPPLY: SIGNAL TIMING IN DE MODE

